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TRANSMITTAL FORM

Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: LX-5

Logic Emulator with Routing Chip providing Virtual Full-Crossbar Interconnect

First Named Inventor: Tan Tseng

SUBMITTED BY

Name:	Stuart T Auvinen
Registration Number:	36,435
Electronic Signature Mark: /es/stuauvinen	Date Signed: 20010105

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Attached Files:

bibd-transmittal	LX5exapds.xml
fee-transmittal	LX5exfee.xml
specification	LX5e.xml
declaration	DeclLX5.tif

FEE TRANSMITTAL

Electronic Version 1.0.4

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Patent fees are subject to annual revisions on or about October 1st of each year.

Small Entity
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TOTAL FEES AUTHORIZED: $ 355

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

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SUBMITTED BY

Authorized Name:	Stuart T Auvinen
Electronic Signature Mark:	/es/stuauvinen
Date Signed:	20010105

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	201	$ 355

Subtotal For Basic Filing Fee: $ 355

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 20	203	$ 9	0	$ 0
Independent Claims: 3	202	$ 40	0	$ 0

Electronic Filing System (EFS) Data

Electronic Patent Application Submission

USPTO Use Only

EFS ID:	10816
Application ID:	09681103
Title of Invention:	Logic Emulator with Routing Chip providing Virtual Full-Crossbar Interconnect
First Named Inventor:	Tan Tseng
Domestic/Foreign Application:	Domestic Application
Filing Date:	null
Effective Receipt Date:	2001-01-05
Submission Type:	Utility Patent Filing
Filing Type:	new-utility
Confirmation Number:	0
Attorney Docket Number:	LX-5
Digital Certificate Holder:	cn=Stuart Thomas Auvinen, ou=Registered Attorneys, ou=Patent and Trademark Office, ou=Department of Commerce, o=U.S. Government, c=US
Certificate Message Digest:	Qvu63Rn6rf4xCvyTzEkFKw==

Document Evaluation Copy

Attached Image File(s):

DeclLX5.tif

Document ID: 2

Comments:

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Document ID: 10/603,802

DECLARATION FOR PATENT APPLICATION LX-5

As the below-named inventor, I hereby declare that:

My residence, mailing address, and citizenship are as stated below next to my name.

I believe that I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention, the specification of which is attached hereto and which has the following title:

Logic Emulator with Routing Chip providing Virtual Full-Crossbar Interconnect

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment specifically referred to in the oath of declaration. I acknowledge a duty to disclose information which is material to examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby appoint the following Agent to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:

Stuart T. Auvinen, Reg. No. 36,435

Address all telephone calls to Stuart T. Auvinen at telephone No. (831) 476-5506.

Address all correspondence to:

Stuart T. Auvinen
429 Twenty-Sixth Ave.
Santa Cruz, CA 95062

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, Section 1001, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Sole/First Inventor's Signature _____ date: 1/6/2000

Full name of first inventor: Tan Tzeng
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Subtotal For Extra Claims Fees: $ 0